



DMP200

FEATURES

- EXTREMELY FLAT GAIN AND LINEAR PHASE RESPONSE
- LOW INPUT VOLTAGE NOISE:
- LOW DISTORTION
- FAST 12-BIT SETTLING:
- LOW DC VOLTAGE OFFSET

DESCRIPTION

The DMP200 provides a convenient amplifier with dynamic range for various amplifications. With SMA connections this box provides low distortion and noise for amplifying those lab signals where bandwidth and pulse response is important. The extended range of the DMP200 provides fast settling for connecting to 12 bit ADC. The low dc offset makes this box easy to interface to existing lab equipment without ac coupling the signal.

APPLICATIONS

- Pulse amplifiers
- Base band and video communications
- Photodiode – photomultiplier preamps
- High Resolution Graphics
- Test Instruments

The DMP products can be order in a variety of gain settings or in some models 300 customize gain operation within the lab.

DMP PRODUCTS

	Small Signal Gain	TYPICAL NOISE FIGURE	-3dB BANDWIDTH PRODUCT (MHz)
100A	20dB	6dB	500
200	20dB	6dB	600
300	28dB	16dB	1200
700	>40dB		700 @ G=2

ABSOLUTE MAXIMUM RATINGS¹

Power Supply.....	$\pm 15V_{DC}$
Differential Input Voltage.....	± 1.2
Input Voltage Range.....	± 3.0
Storage Voltage Range:	-40°C to +125°

ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Its recommended that all boxes be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

ORDERING INFORMATION

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR ¹	Gain Setting	ORDERING NUMBER	QUANTITY
DMP200	SMA	"	20dB		

ELECTRICAL CHARACTERISTICS: $V_S = \pm 15V$

At $T_A = +25^\circ C$, $V_S = \pm 15V$, $R_L = 100\Omega$,

PARAMETER	CONDITIONS	TYP			UNITS	MIN/MAX
		25°C	Min	Max		
Small-Signal Bandwidth ($V_o=0.5V_{p-p}$)	$G=20$	625	600		MHz	typ
Bandwidth for 0.2dB Gain Flatness	$G = +20, R_L = 100\Omega, V_o = 500mV_{p-p}$	420	400		MHz	min
Large Signal Bandwidth	$G=+20, V_o=4.0V_{p-p}$	525	500		MHz	typ
Harmonic Distortion	$G = +20, f = 10MHz, V_o = 2V_{p-p}$					
2 nd -Harmonic	$R_L = 50\Omega$	-65	-62		dBc	max
	$R_L > 500\Omega$	-78	-76		dBc	max
3 rd -Harmonic	$R_L = 50\Omega$	-86	-84		dBc	max
	$R_L = 500\Omega$	-86	-82		dBc	max
2-Tone, 3 rd -Order Intercept	$G = 20dB$ to load, $f = 100MHz$	33			dBm	min
Total Input Referred Voltage Noise					nV/\sqrt{Hz}	max
Noise Figure		6		7	dB	typ
Rise-and-Fall Time	$G=+20, V_o=0.5V$ Step	0.8			ns	max
	$G=+20, V_o=4V$ Step	1.0			ns	max
Slew Rate	$G=+20, V_o=4V$ Step	4300			V/ μs	min
Settling Time to 0.02%	2V Step	16			ns	typ
0.1%	2V Step	10			ns	max
DC PERFORMANCE⁽⁴⁾						
Input Offset Voltage	$V_{CM} = 0V$	± 0.001	± 0.01	± 0.025	mV	max
Average Offset Voltage Drift	$V_{CM} = 0V$			± 20	$\mu V/^\circ C$	max
INPUT						
Common-Mode Input Range(CMIR) ⁽⁵⁾	Infinite Load	± 3.2	± 3.0	± 2.9	V	min
Common-Mode Rejection (CMR)	$V_{CM} = \pm 1V$, Input Referred	95	85	84	dB	min
Input/Output Impedance		50			Ohm	typ
OUTPUT						
Power Output	At -1dB compression	17			dBm	
Current Output, Sourcing	$V_o = 0V$	± 100	± 90	± 85	mA	min
POWER SUPPLY						
Specified Operating Voltage		± 15			V	typ
Maximum Operating Voltage		± 15			V	max
Minimum Operating Voltage		± 9			V	min
Max Quiescent Current	$V_S = \pm 15V$				mA	max
+PSRR, -PSRR	$ V_{S1} = 4.5$ to 5.5 , Input Referred	100	90	88	dB	min





